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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application / Conf. No.	10/655,637 /
Filing Dat	September 05, 2003
First Named Inventor	Mingdeng Chen
Art Unit	*Unknown- 28/6
Examiner Name	Unknown Wells
Attorney Docket Number	X-1501 US

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Examiner Initials *	Cite No.'	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the Item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T?
KAN		SHAHRIAR JAMASB, A 622MHz Stand-alone LVDS Driver Pad in 0.18-um CMOS, IEEE, 3 Park Avenue, 17th Floor, New York, NY., 10016-5997, 2001, Conexant Systems and University of San Diego, San Diego, California.	
con		ANDREA BONI, LVDS I/O Interface for Gb/s-per-Pin Operation in 0.35-um CMOS, IEEE Journal Of Solid-State Circuits, April 2001, Vol. 36, No. 4, IEEE, 3 Park Avenue, 17th Floor, New York, NY., 10016-5997.	
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

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INFOR	INFORMATION DISCLOSURE			Filing Date	September 05, 2003	
STATEMENT BY APPLICANT		First Named Inventor	Mingdeng Chen			
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Sheet	1	of	1	Attorney Docket Number	X-1501 US	

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Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	τ,		
Kam		BONI, ANDREA; "1.2-Gb/s True PECL 100K Compatible I/O Interface in 0.35-um CMOS," IEEE Journal of Solid State Circuits, Vol. 36, No. 6, June 2001, pages 979-987, IEEE, 445 Hoes Lane, Piscataway, NJ 08854-1331,USA			
Kom		DJAHANSHAHI, HORMOZ; "Gigabit-per-Second, ECL-Compatible I/O Interface in 0.35-um CMOS," IEEE Journal of Solid State Circuits, Vol. 34, No. 8, August 1999, pages 1074-1083, IEEE, 445 Hoes Lane, Piscataway, NJ 08854-1331,			
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